

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SRIRAM VAJAPÉYAM, ET AL.

Application No.: 10/037,666

Filed: January 3, 2002

For: **DEPENDENCE-CHAIN PROCESSOR USING TRACE  
DESCRIPTOR(S) HAVING DEPENDENCY DESCRIPTOR(S)  
(AS AMENDED)**



Art Group: 2183

Examiner: David I. Huisman

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
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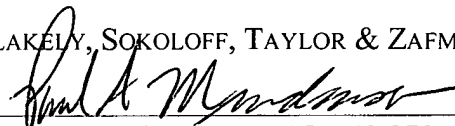
In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



**Theresa Belland**

03-08-06

**Date**



Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
				Application Number	10/037,666
				Filing Date	January 3, 2002
				First Named Inventor	Sriram Vajapeyam
				Art Unit	2183
				Examiner Name	David I. Huisman
Sheet	2	of	2	Attorney Docket Number	42390P11927

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
		Black, Bryan, et al., "The Block-based Trace Cache", Proceedings of the 26th International Symposium on Computer Architecture, pp. 196-207 (May 2-4, 1999)		
		Smith, James E., et al., "Trace Processors: Moving to Fourth-Generation Microarchitectures", Computer, Vol. 30, Issue 9, pp. 68-74 (Sep. 1997).		
		Vajapeyam, Sriram, et al., "Dynamic Vectorization: A Mechanism for Exploiting Far-Flung ILP in Ordinary Programs", Proceedings of the 26th International Symposium on Computer Architecture, pp. 16-27 (May 2-4, 1999)		
		U.S. Application No. 10/950,693, entitled SYSTEM, METHOD AND APPARATUS FOR DEPENDENCY CHAIN PROCESSING, filed September 28, 2004, by Satish Narayanasamy et al.		

Examiner Signature		Date Considered	
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
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